

Biodata

Name	Dr. Shany Joseph 
Designation	Scientist D
Educational qualification	BE (Chemical) – NIT, Raipur M. Sc.(Engg) – Material Science – Indian Institute of Science, Bangalore Ph.D in Electronic Science, Pune University
Research area	<ul style="list-style-type: none"> • Electroplating of Lead-free solders • Solder Bumping • Thick film materials • Solder pastes • Electronics Packaging • Low Temperature Co-fired Ceramic (LTCC) processes and Materials
Recognised Awards/Honors/Fellow	<ul style="list-style-type: none"> • Life Member, ISSS • Life Member, MRSI
Projects	<p>Ongoing:</p> <ol style="list-style-type: none"> 1. Development of Solid oxide fuel cells (SOFC) using LTCC technology (Co-PI), DST, Rs. 210.17 lakhs, (Nov. 2019-Nov. 2022). 2. LTCC based devices for “Integrated low cost water sensors for real time water monitoring and decision making (Co-PI), IUSSTF (DST), Rs 36.97 lakhs, June 2018-June 2021. 3. Development of LTCC based 3D Printing technology for low cost optoelectronic packaging, Cent (Co-PI), Rs 488.17 lakhs, Aug. 2020- Aug. 2023. <p>Completed:</p> <p>Number of projects completed till December 2014. (12Nos)</p> <p>Completed projects from Jan 2015 to till date:</p> <ol style="list-style-type: none"> 1. Development of Sn-Ag-Cu based lead-free electrolyte for Surface finishing of PCBs (PI), DST, Rs. 69.25 Lakhs, May 2016-Dec 2019. 2. Development of CNT-lead-free composites for Flip chip applications (PI), Working group MEITY, Rs. 62 lakhs, Dec 2014-March 2017. 3. Development of Magnetic sensors in LTCC (Co-PI), BARC, Rs. 169.29 lakhs, Jan 2015-July 2018). 4. LTCC based Pressure Sensor (Co-PI, M/s Eaton Technologies (P) Ltd, Rs. 35.63 lakhs, May 2015- July 2017). 5. General Purpose LTCC materials (Co-PI), DST & CMET, Rs. 651.58 lakhs) Dec 12- Dec 2017.

	<p>6. Fabrication of multilayer RF circuits on LTCC (Co-PI), SAC, ISRO Rs. 45 lakhs), June 2011- March 2019.</p> <p>7. Prototype package fabrication in LTCC (Co-PI), BARC, Rs. 198.43 lakhs, Nov. 09- July 2015.</p>
<p>Publications/Patents (Past 5 years)</p>	<p><i>Patents List (Past 5 years)</i></p> <ol style="list-style-type: none"> 1. A Non-conductive substrate with tracks formed by sand blasting, Girish Phatak, Shrikant Kulkarni, Vijaya Giramkar, Shany Joseph <ul style="list-style-type: none"> • US Patent Appl. No.15/542,567, Appln date 7th July 2017, Patent approved, NoA issued • European Patent Appl. No. 16737136.8, Appln date :18th July 2017 • Indian Patent Appl. No. 130/MUM/2015, Appln date: 13th Jan 2015 • PCT Appl. No.PCT/IB2016/050083, Appln date 8th Jan 2016 2. A Non-Conductive Substrate with Conductive Tracks Formed by Laser Ablation Method, Shany Joseph, Payal Bhawtankar, Adwait Shitole, Adwaita Jadhav, Vijaya Giramkar and Girish Phatak, Indian Patent Appl. No. 2335/MUM/2015, Appl Date: 18th June 2015, Publication Date: 23rd Dec 2016